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(Also Form PTO-1050)

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

CERTIFICATE OF CORRECTION	
	Page <u>1</u> of <u>1</u>
PATENT NO. : 7,605,416	<u> </u>
APPLICATION NO.: 10/531,442	
ISSUE DATE : October 20, 2009	
INVENTOR(S) : Mun-Pyo HONG, et al.	
It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:	
The title of the invention should read "THIN FILM TRANSISTOR ARRAY PANEL AND A METHOD FOR MANUFACTURING THE PANEL"	

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Cantor Colburn LLP 20 Church Street, 22nd Floor Hartford, CT 06103

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